

CLAIMS

- 1 1. A transceiver circuit, said circuit comprising:
 - 2 a transmitter, said transmitter transmitting a normal link pulse during idle period;
 - 3 a number of clock signals, each controlling a portion of said transceiver; and
 - 4 said transceivers circuit utilizing said clock signals for minimizing power
 - 5 consumption.

Sub A2 >

66004 66004 66004